



Productronic



EURIPIDES Forum 2008


Electronics Production Equipment – Markets, Technologies and Backgrounds

October 9, 2008
 Dr. Eric Maiser
 VDMA Productronic Association

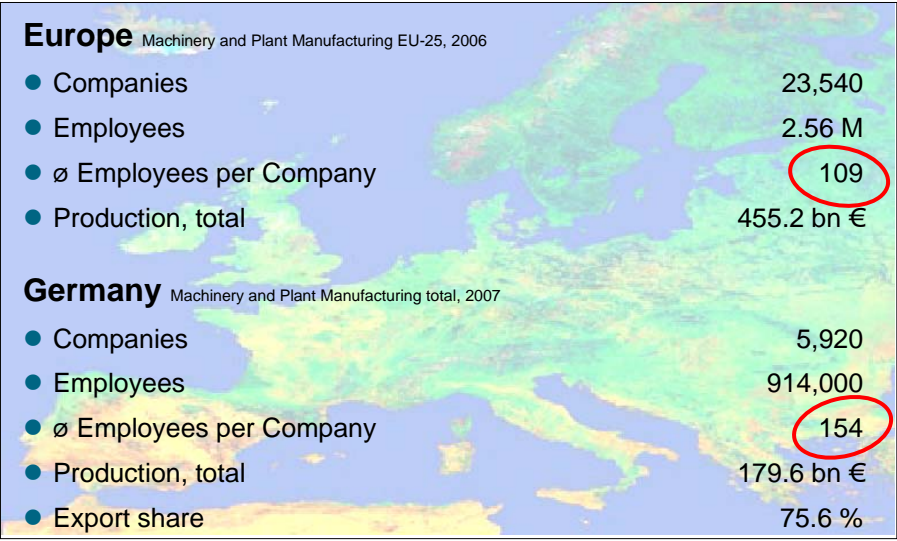


Picture: Darren Baker, Charles Taylor, Michael Morris, fotolia.com

VDMA – Wir, die Investitionsgüterindustrie



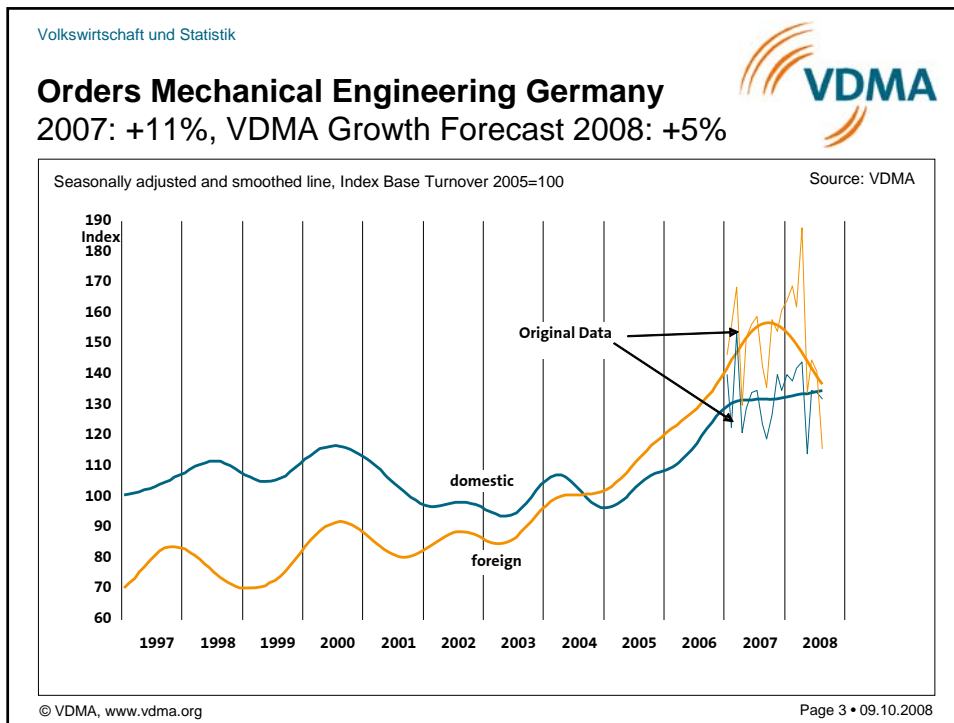
Machinery and Plant Manufacturing “Enabling Industry”, acting globally



Europe		Machinery and Plant Manufacturing EU-25, 2006	
● Companies			23,540
● Employees			2.56 M
● ø Employees per Company			109
● Production, total			455.2 bn €
Germany		Machinery and Plant Manufacturing total, 2007	
● Companies			5,920
● Employees			914,000
● ø Employees per Company			154
● Production, total			179.6 bn €
● Export share			75.6 %

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VDMA – The German Engineering Federation



**Our Industry Platform
for Machinery and Plant Manufacturing**

- Servicing since 1892
- More than 3,000 member companies along the value chain from components to entire plants
- 38 sector associations represent the industry
- 450 motivated staff in Germany, Europe and overseas
- 22,000 in honorary appointments
- 1,540 Events per year, 46,000 visitors



Largest Network of our Industry

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VDMA – Wir, die Investitionsgüterindustrie

VDMA industry association
Fields of action




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VDMA – Wir, die Investitionsgüterindustrie

VDMA Organisation Structure and Service Portfolio




- Topics for all equipment makers
 - 9 horizontal departments (tax, foreign trade, legal...)
- Sector-specific topics
 - 38 sector associations
 - 9 cross-sectorial forums
 - 17 working groups
- Contact persons close to you
 - 6 regional offices
- Presence in Europe and overseas
 - German capital office, Berlin
 - European Office Brussels
 - Liaison Offices
 - Tokyo, 3x India, Beijing, Shanghai, Moscow, Gulf region
- 11 service companies (exhibitions, conferences, insurances, publishing house...)
- 17 research societies
- 7 foundations



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VDMA – The German Engineering Federation



Diverse Machinery Industry: VDMA sector associations and groups

- Agricultural technology
- Air-handling technology
- Building control and management
- Cleaning systems
- Compressors and vacuum technology
- Construction equipment and building materials machines, glass
- Electrical automation
- Energy
- Engines and Systems
- Equipment for metallurgical plants and rolling mills
- Fire fighting equipment
- Flat Panel Displays
- Fluid power
- Food processing & packaging machinery
- Foundry machinery
- Garment and leather technology
- German Flat Panel Display Forum
- Lifts and escalators
- Lasers and laser systems
- Machine tools & manufacturing systems
- Materials handling & logistic technology
- Micro technology
- Mining equipment
- Municipal Equipment
- Organic Electronics Association
- Plastics and rubbers technology
- Power systems
- Power transmission engineering
- Precision tools
- Printing & paper equipment and supplies
- Process plant and equipment
- **Electronics production equipment (Productronic)**
- Pumps + Systems
- Robotics + Automation
- Surface treatment technology
- Security Systems
- Software
- Testing technology
- Textile machinery
- Thermo process and waste treatment technology
- Valves
- Weighing technology
- Welding
- Woodworking machinery

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VDMA “Innovative Business”



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Micro technology



Flat Panel Displays



Organic Electronics

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Productronics Association
We enable Electronics Production

VDMA

Back End

- Semiconductor components (ICs)
- Flat Panel Displays (FPD)
- Micro systems (MEMS)
- Data storage
 - optical: CD, DVD
 - magnetical: HDD, FDD
- Sensors
- Smart Cards
- Photovoltaics

Front End

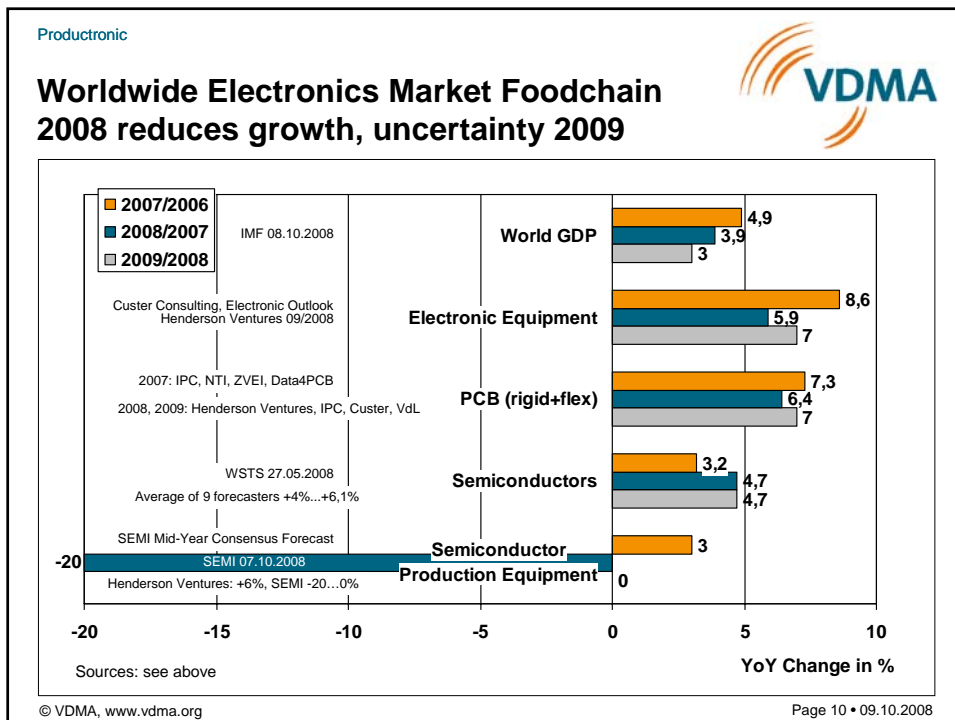
- Printed Circuit Boards (PCB)
- Surface Mount Technology (SMT)
- Packaging
- Hybrids

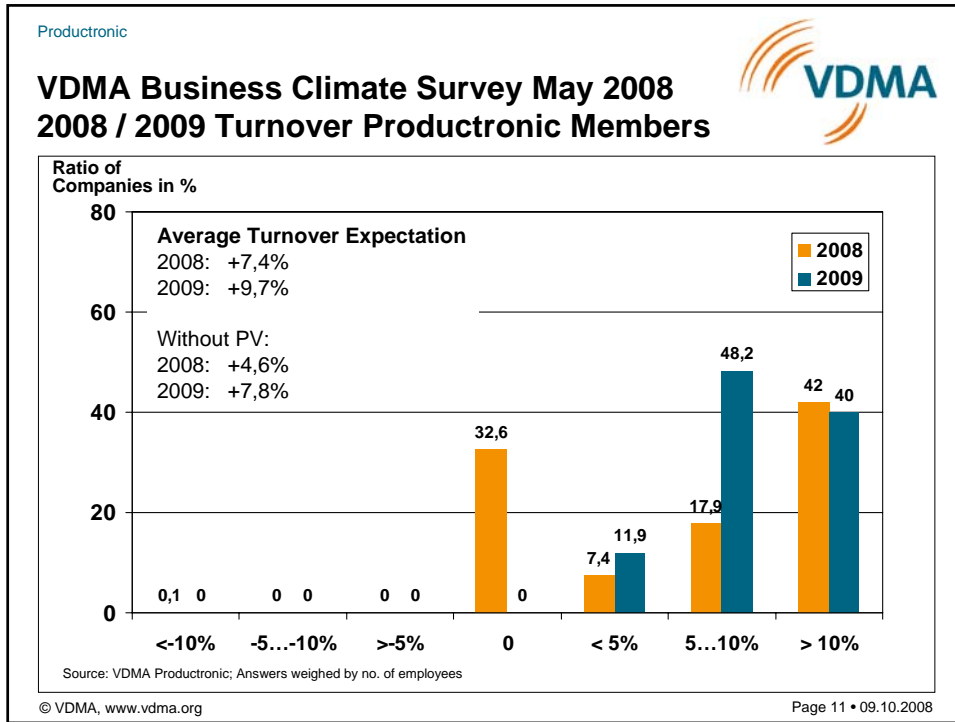
www.productronics.org

65 Member Companies

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Pictures: Meiser, fotolia





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2004 ITRS Summary 1

2004 ITRS SUMMARY

2004 UPDATE OVERVIEW

The ITRS process is an ongoing event. The industry is dynamic—continually innovating, introducing new products, and achieving solutions. To keep the ITRS information as current as possible with this dynamic industry environment, during each year following an edition such as the 2003 ITRS, the roadmap information is reviewed. Data adjustments, corrections, and new information items are agreed to among the ITWG members and by soliciting public feedback during the annual ITRS Summer Conference in San Francisco. For the 2004 Update effort, all the ITRS tables were reviewed. If necessary, data and notations were updated to match industry advancements.

[Link to the 2003 ITRS](#)

Key Changes of the 2004 Update

A significant task was performed during this past year by annualizing all the tables. Previous years for the “long-term” technology requirements tables were indicated only for those years of technology generations, also referred to as “node” years. New for the 2004 Update, and set in place for the more involved effort of the complete revision of the 2005 ITRS, all 15 years of the ITRS timeframe are now indicated in all tables.

All chapters of the 2003 ITRS have updates. Of the 219 tables contained in the 2003 ITRS, 128 tables were updated and 4 new tables were added. Additionally, 12 figures were updated. Many of the difficult challenges for the ITRS were reviewed and updated to reflect the most current thinking and most critical tasks for semiconductor R&D. The *Overall Technology Roadmap Characteristics* were updated to reflect chapter table updates as well as indicate some revisions to be consistent with the technology tables. Please refer to the Appendix for the complete listing of ITRS tables and updates.

The reader should realize, that for the 2003 ITRS, the Near-term years span from 2003 to 2009 while the Long-term years span from 2010 to 2018. The reader will notice however that the year 2010 represents the year in which the 4p45 technology node will occur. Several ITWG's have included any consideration related to this technology node in their Near-term challenges since it was easier for many of them to concentrate their attention on a well-defined technology node. Normally the technology nodes are stated very atomically while the data shown in the tables for the intermediate years are obtained by interpolation.

During this global review of the 2003 ITRS, a key message is that many of the difficult challenges and technology requirements were verified as correctly assessed for the semiconductor industry.

Although the update years focus on the table values, several chapters added new topics. A section introducing Emerging Research Materials was added to the Emerging Research Devices chapter, which was reviewed and updated in its entirety.

CD control tolerances and the timing of the introduction of 450nm were pervasive discussions among the teams. Edge exclusion was aligned among Front End Processes, Lithography, Factory Integration, and Yield Enhancement.

Other specific changes and challenges among the teams are as follows:

The Design chapter is revised to include an updated section on Design for Manufacturability and Design Verification.

**“The mother of all Roadmaps”:
The International Technology Roadmap for Semiconductors (ITRS)**

<http://public.itrs.net>

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Table 72b DRAM Stacked Capacitor Films Technology Requirements—Long-term UPDATED

Year of Production	2010	2011	2012	2013	2014	2015	2016	2017	2018
Technology Node	4p45	4p40	4p35	4p32	4p28	4p25	4p22	4p20	4p18
WAS Dielet. Perm. const. (H)	6	6	6	6	6	6	6	6	6
IS Yield size factor (H)	6	6	6	6	6	6	6	6	6
WAS Yield size (µm ²) (F)	0.912 +0.09/-0.14	0.810	0.707 +0.07/-0.11	0.606 +0.06/-0.1	0.505 +0.05/-0.09	0.404 +0.04/-0.08	0.303 +0.03/-0.07	0.202 +0.02/-0.06	0.101 +0.01/-0.05
IS Yield size (µm ²) (F)	0.912 +0.09/-0.14	0.810 +0.08/-0.12	0.707 +0.07/-0.11	0.606 +0.06/-0.1	0.505 +0.05/-0.09	0.404 +0.04/-0.08	0.303 +0.03/-0.07	0.202 +0.02/-0.06	0.101 +0.01/-0.05
WAS Storage node size (µm ²) (F)	0.004 +0.045/-0.09	0.004	0.002 +0.025/-0.07	0.002 +0.022/-0.06	0.001 +0.020/-0.05	0.001 +0.018/-0.05	0.001 +0.016/-0.04	0.001 +0.014/-0.04	0.0005 +0.012/-0.03
IS Storage node size (µm ²) (H)	0.004 +0.045/-0.09	0.002 +0.043/-0.09	0.002 +0.038/-0.07	0.002 +0.032/-0.06	0.002 +0.028/-0.05	0.001 +0.023/-0.05	0.001 +0.019/-0.04	0.001 +0.015/-0.04	0.0005 +0.011/-0.03
WAS Capacitor structure	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM
IS Capacitor structure	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM
WAS Capacitor dielectric material	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO
IS Capacitor dielectric material	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO	ALQTAO
WAS Layer 250 (nm) (H)	0.7	0.7	0.6	0.5	0.4	0.4	0.4	0.3	0.25
IS Layer 250 (nm) (H)	0.7	0.7	0.6	0.5	0.4	0.4	0.4	0.3	0.25
WAS Dielectric constant	50	50	50	50	50	50	50	50	50
IS Dielectric constant	50	50	50	50	50	50	50	50	50
WAS IS dielectric loss	1.9	2	2	2	2	2	2	2	2
IS IS dielectric loss	1.9	2	2	2	2	2	2	2	2
WAS Yield size factor (H)	1	1	1	1	1	1	1	1	1
IS Yield size factor (H)	1	1	1	1	1	1	1	1	1
WAS Roughness factor	1	1	1	1	1	1	1	1	1
IS Roughness factor	1	1	1	1	1	1	1	1	1
WAS Total capacitor area (µm ²)	0.52	0.48	0.42	0.39	0.34	0.3	0.26	0.2	0.18
IS Total capacitor area (µm ²)	0.52	0.48	0.42	0.39	0.34	0.3	0.26	0.2	0.18
WAS Structural coefficient (F)	42.6	50.3	57.5	62.8	71.8	80.3	91.2	100.3	111.4
IS Structural coefficient (F)	42.6	50.3	57.5	62.8	71.8	80.3	91.2	100.3	111.4
WAS Yield size 250 (nm) (H)	9.2	10.3	9	8.2	9.6	8.5	7.5	7.1	6.4
IS Yield size 250 (nm) (H)	9.2	10.3	9	8.2	9.6	8.5	7.5	7.1	6.4
WAS I/R of SS (A/T) for cell plane (µm)	71.2	102.7	117.3	128.3	142	162	188	216	245
IS I/R of SS (A/T) for cell plane (µm)	71.2	102.7	117.3	128.3	142	162	188	216	245
WAS I/R dielectric layer (H)	0.65	0.65	0.64	0.64	0.63	0.63	0.63	0.62	0.62
IS I/R dielectric layer (H)	0.65	0.65	0.64	0.64	0.63	0.63	0.63	0.62	0.62

Manufacturable solutions exist, and are being optimized

Manufacturable solutions are known

Interim solutions are known


Manufacturable solutions are NOT known

**“The mother of all Roadmaps”:
The International Technology Roadmap for Semiconductors (ITRS)**

introducing
**“red brick walls”
(technology breakthroughs)**

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Roadmapping Methodology

- Semiconductors:
Sandia Labs Technology Roadmapping Process paper
“Technology roadmapping is driven by *needs*, not by solutions”
- for further reading:
 - Garcia, Marie L., 1997, “Introduction to Technology Roadmapping: The Semiconductor Industry Association’s Technology Roadmapping Process”, SAND97-0666. Sandia National Laboratories, Albuquerque, NM.
 - R. N. Kostoff, R. R. Schaller, “Science and Technology Roadmaps” IEEE Transactions on Engineering Management, Vol. 48, No. 2 (2001), pp. 132
 - Gerrit Muller: “Roadmapping”, 2003

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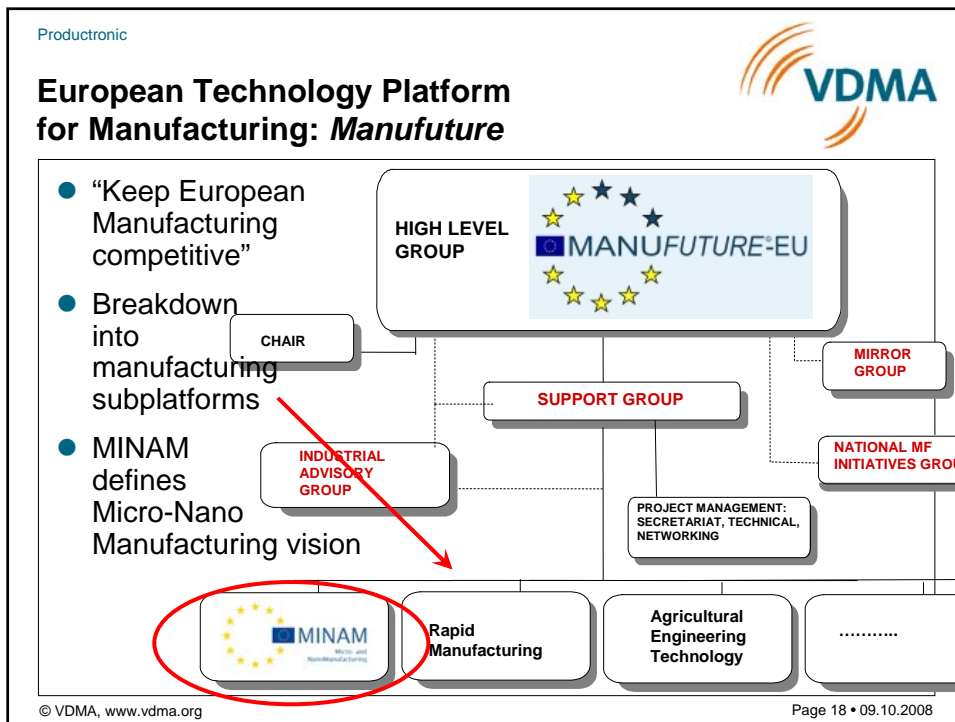
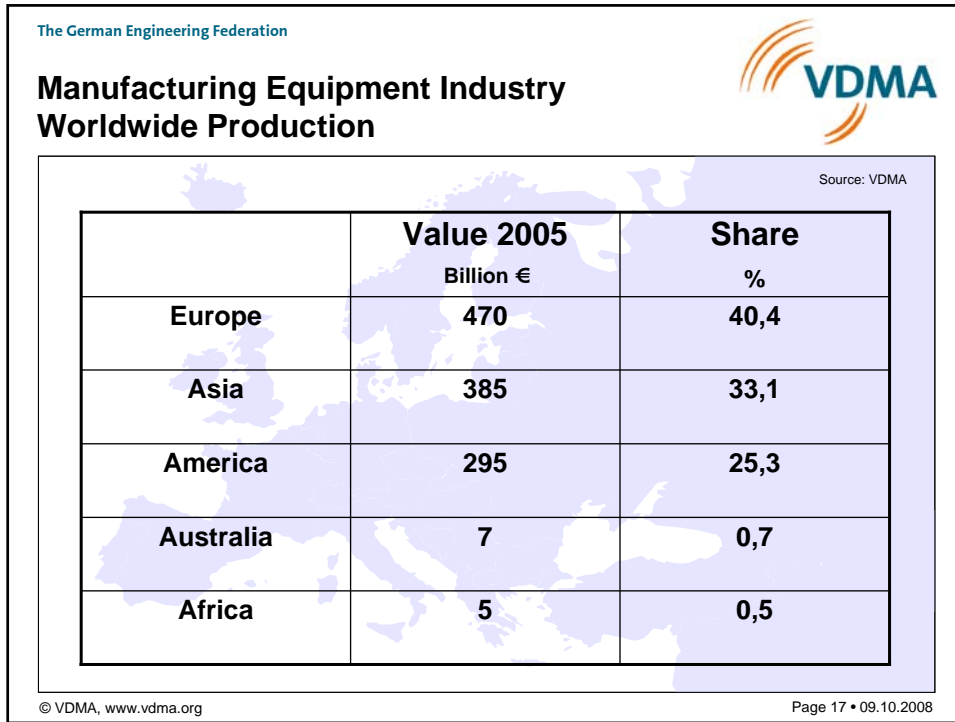


Selection of European Platforms with VDMA Participation


- ADRIA (Flat Panel Displays)
Vision paper, May 2005
Roadmap, 250 pages, March 2007
www.adria-network.org
- **MINAM (Micro-/Nano-Manufacturing) SRA, 190 pages, January 2008**
www.micronanomanufacturing.org
- Manufuture (Production Technology)
www.manufuture.org
- **Organic Electronics (printed / flexible electronics)**
2nd Edition, July 2008
www.vdma.org/oe-a → downloads
3rd Edition, June 2009
Conference LOPE-C
- Photonics
www.photonics21.org

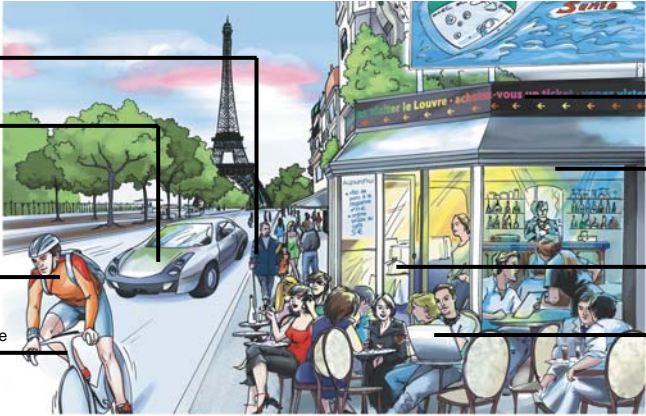


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Hot topics I: Micro- NanoManufacturing “Enabling affordable future products”





Hip Joint from biocompatible plastics

Fuel Cells for mobile phone and mobility

Intelligent Clothing

BuckyTube frame lightweight and robust

Thin film displays and lighting

Scratchproof window with Lotus-Effect

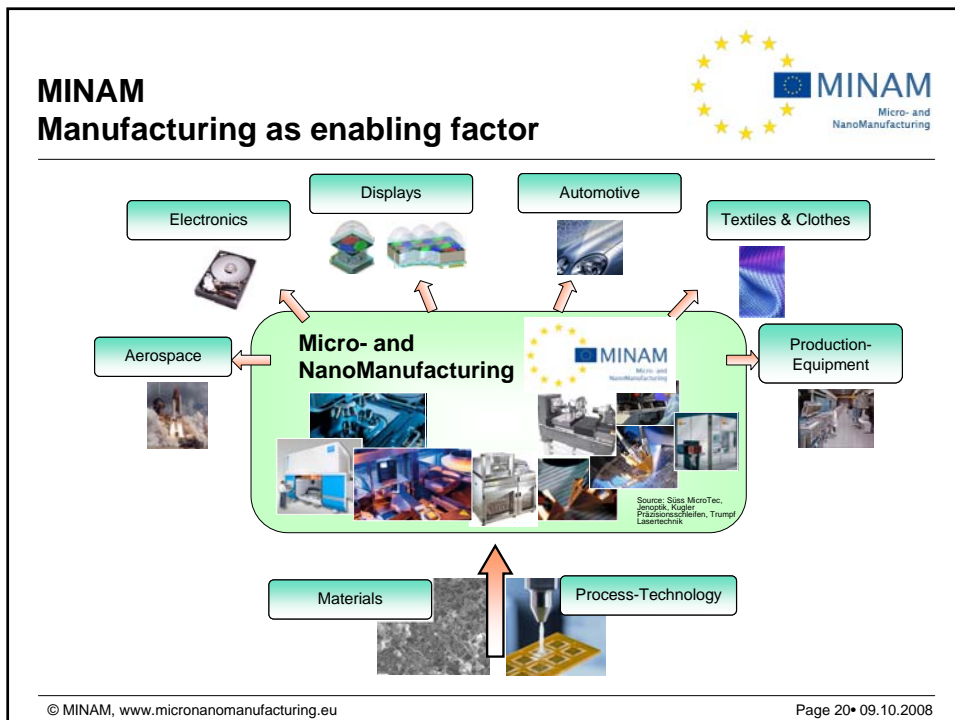
LEDs save energy

Nanotubes for Notebooks

- How to coordinate research?
- How to maintain European leadership?
- How to get from lab to fab?

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Source: Siemens AG, Fraunhofer IPA 09.10.2008



MINAM

Towards a common vision

- Roadmapping: defining needs and creating a vision
- Comparing needs with technology evaluation reveals roadblocks
- Targeted Research removes roadblocks



Roadmap
"Strategic Research Agenda"

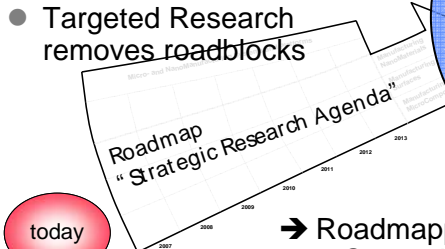
→ Roadmapping is the essential tool to define a *Strategic Research Agenda*

→ MINAM Roadmap published 01.2008

Vision

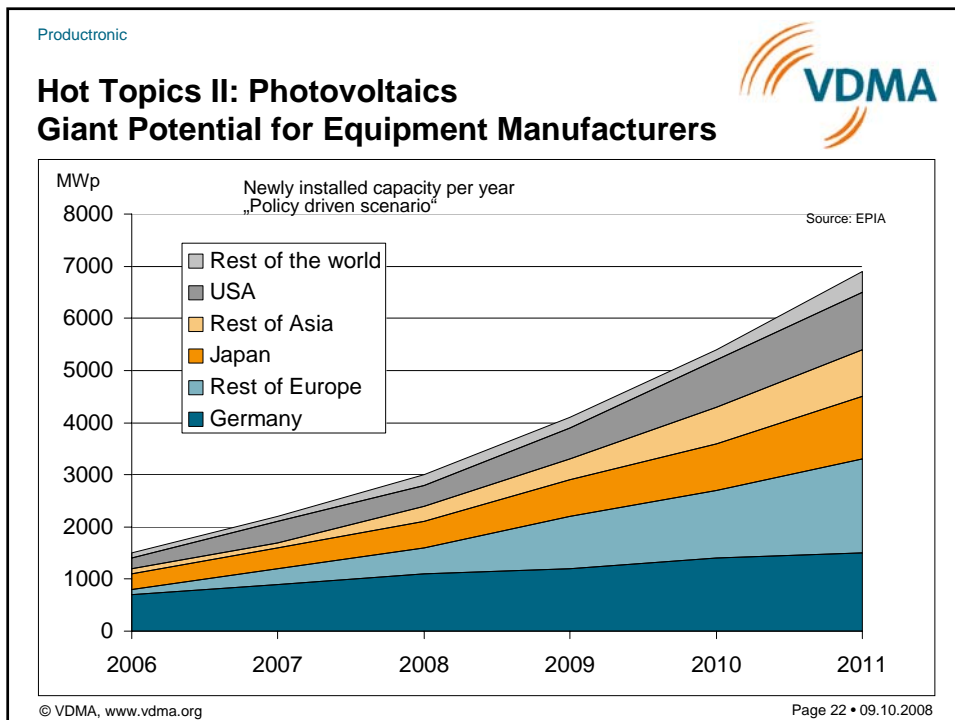
The European community of Micro- and NanoManufacturing aims at the **worldwide leadership of European manufacturers and equipment-suppliers** in the field of manufacturing micro- and nano-technological products – It will be **the European network** in these fields.

today



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Photovoltaic Equipment Europe in Excellent Position

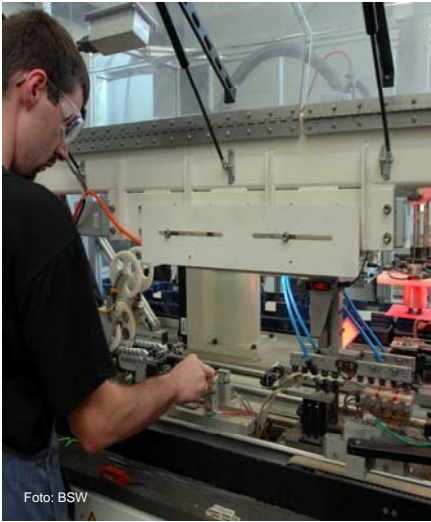


Foto: BSW

- European machinery and plant manufacturers are forerunners
- More than 80 companies in Germany
- Excellent growth potential
 - significant domestic market
 - but also massive increase in export to Europe (Spain, Italy), and the USA
- VDMA Forum Photovoltaic Equipment
 - Cross-sectoral organisation with 9 VDMA associations involved

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European Technology Platform Photovoltaics





- European Technology Platform for Photovoltaics
Strategic Research Agenda (SRA)
www.eupvplatform.org
 - Working Group 3
 - Published September 2007



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VDMA – Organic Electronics Association

Hot Topics III: Organic / Printed Electronics

Vision: „Electronics Everywhere“

oe-a
Organic Electronics Association

Organic Electronics Applications

- thin, lightweight, flexible
- low-cost, large-area, printing processes
- **First products** step into the markets now:
 - RFID tags, memory, sensors
 - Games
 - Rollable displays
 - Organic photovoltaics, batteries

Organic Electronics Association

- A working group within VDMA
- **International key industry association** for organic & printed electronics
- more than 100 members along the value chain from Europe, North America, Asia
- **www.oe-a.org**

Pictures: pmTUC, PolyIC

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VDMA – Organic Electronics Association

Organic Electronics

A Future Multi-Billion \$ Market

oe-a
Organic Electronics Association

- Organic electronics enables **new applications** and opens up **new markets**
- Market for Organic and Printed Electronics is expected to grow to 300 Bn US \$ by 2027, driven by logic, memory, displays and lighting
- Market will take similar path as Semiconductors and FPDs in the past
→ Experience factor of these industries
- But will also profit from strong printing industry in Europe
→ Results in very good chances for a new European industry!

Sources: DisplaySearch, SEMI, SIA von Custer, VLSI SEMICON Europa, NanoMarkets, IDTechEx

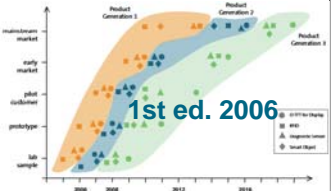

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VDMA – Organic Electronics Association

Second Edition OE-A Roadmap for Organic Electronics

oe-a
Organic Electronics Association

- Supplement and improvement of the first version presented in 2006
- Represents common view of OE-A members
- Future applications and product generations
- Key application parameters
- Key technology parameters
- Red brick walls

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VDMA – Organic Electronics Association

Applications: Evaluation of Seven Topics

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Organic photovoltaic cells
for mobile + stationary use




Organic memory devices
for consumer goods



Smart objects
as combination
of different devices



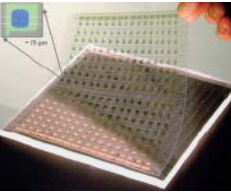
Organic sensors
for single use devices



Printed RFID for brand
protection and logistics



Flexible batteries
to power mobile devices



**Organic TFT
backplanes** for displays

© OE-A, www.oe-a.org Source: Konarka, TFE, Poly IC, MRS Page 28• 09.10.2008

Key Challenges / Red Brick Walls



Red Brick Walls:

Major breakthroughs are absolutely necessary:

- **Resolution, registration and process stability of the patterning processes**
 - resolution of 10 μm and smaller with appropriate registration accuracy is necessary @ mass production
- **Charge carrier mobility and electrical conductivity of the semiconductor and conducting materials**
 - mobility > 1 cm^2/Vs for solution processable semiconductors @ mass production
 - 5 – 10 cm^2/Vs would largely push the topic enabling more complex devices
- **Circuit design including CMOS-like transistors**
- **New strategies for quality control**



Table 72b DRAM Stacked Capacitor Films Technology Requirements – Long-term LTPD/ALTD

Year of Production	2010	2011	2012	2013	2014	2015	2016	2017	2018
Technology Node	45	40	35	32	28	25	22	20	18
WAS	6	6	6	6	6	6	6	6	6
IS	6	6	6	6	6	6	6	6	6
WAS	0.912 +0.09x-0.14	0.810 +0.08x-0.11	0.707 +0.07x-0.11	0.604 +0.06x-0.1	0.505 +0.05x-0.09	0.404 +0.04x-0.08	0.303 +0.03x-0.07	0.202 +0.02x-0.06	0.101 +0.01x-0.05
IS	0.912 +0.09x-0.14	0.810 +0.08x-0.11	0.707 +0.07x-0.11	0.604 +0.06x-0.1	0.505 +0.05x-0.09	0.404 +0.04x-0.08	0.303 +0.03x-0.07	0.202 +0.02x-0.06	0.101 +0.01x-0.05
WAS	0.004 +0.045x-0.09	0.002 +0.025x-0.07	0.002 +0.022x-0.06	0.002 +0.022x-0.06	0.002 +0.022x-0.06	0.001 +0.022x-0.05	0.001 +0.022x-0.04	0.001 +0.022x-0.04	0.0005 +0.018x-0.03
IS	0.004 +0.045x-0.09	0.002 +0.025x-0.07	0.002 +0.022x-0.06	0.002 +0.022x-0.06	0.002 +0.022x-0.06	0.001 +0.022x-0.05	0.001 +0.022x-0.04	0.001 +0.022x-0.04	0.0005 +0.018x-0.03
WAS	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM
IS	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM	Pedestal MIM
WAS	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM
IS	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM	ALQTAO MIM
WAS	0.7	0.6	0.5	0.4	0.4	0.4	0.4	0.4	0.25
IS	0.7	0.6	0.5	0.4	0.4	0.4	0.4	0.4	0.25
WAS	50	50	50	50	50	50	50	50	100
IS	50	50	50	50	50	50	50	50	100
WAS	1.9	2	2	2	2	2	2	2	2
IS	1.9	2	2	2	2	2	2	2	2
WAS	1	1	1	1	1	1	1	1	1
IS	1	1	1	1	1	1	1	1	1
WAS	1	1	1	1	1	1	1	1	1
IS	1	1	1	1	1	1	1	1	1
WAS	0.52	0.48	0.42	0.39	0.34	0.3	0.26	0.2	0.18
IS	0.52	0.48	0.42	0.39	0.34	0.3	0.26	0.2	0.18
WAS	42.6	50.3	57.5	62.8	71.8	80.3	91.2	100.3	111.4
IS	42.6	50.3	57.5	62.8	71.8	80.3	91.2	100.3	111.4
WAS	9.2	10.3	9	8.2	9.6	8.5	7.5	7.1	6.4
IS	9.2	10.3	9	8.2	9.6	8.5	7.5	7.1	6.4
WAS	71.2	102.7	117.3	138.3	162	188	208	246	283
IS	71.2	102.7	117.3	138.3	162	188	208	246	283
WAS	0.65	0.04	0.04	0.04	0.03	0.03	0.03	0.02	0.02
IS	0.65	0.04	0.04	0.04	0.03	0.03	0.03	0.02	0.02

Manufacturable solutions exist, and are being optimized
 Manufacturable solutions are known
 Interim solutions are known
 Manufacturable solutions are NOT known

ITRS:

Breakdown into Materials and Manufacturing Equipment!

Usually not in a lot of other technology roadmaps feature Equipment specs

→ Roadmap coaching for Equipment makers needed!

Productronic



VDMA Productronic Working Group “Technology Scouting”

Focus: Breakdown of Electronics Roadmaps for needs of Manufacturing Equipment Industry


- Identification and prioritisation of topics with relevance for Equipment makers
- Which aspects are missing from manufacturing equipment viewpoint
- What do the roadmaps imply for process technology, which requirements for materials and equipment
- Unsolved challenges

➔ Matrix structure along process chain





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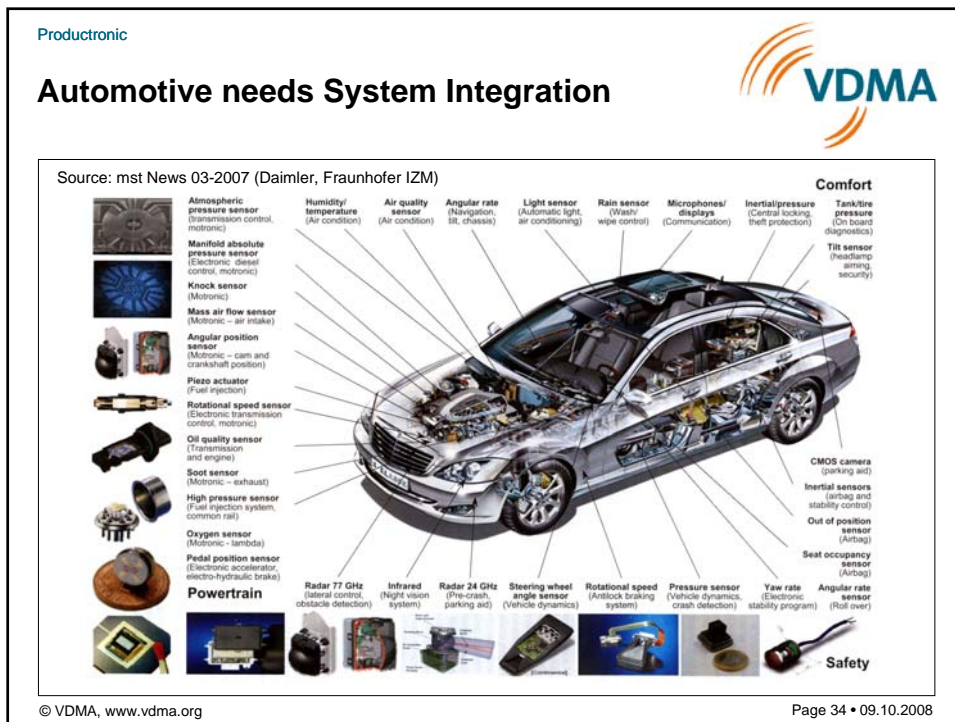
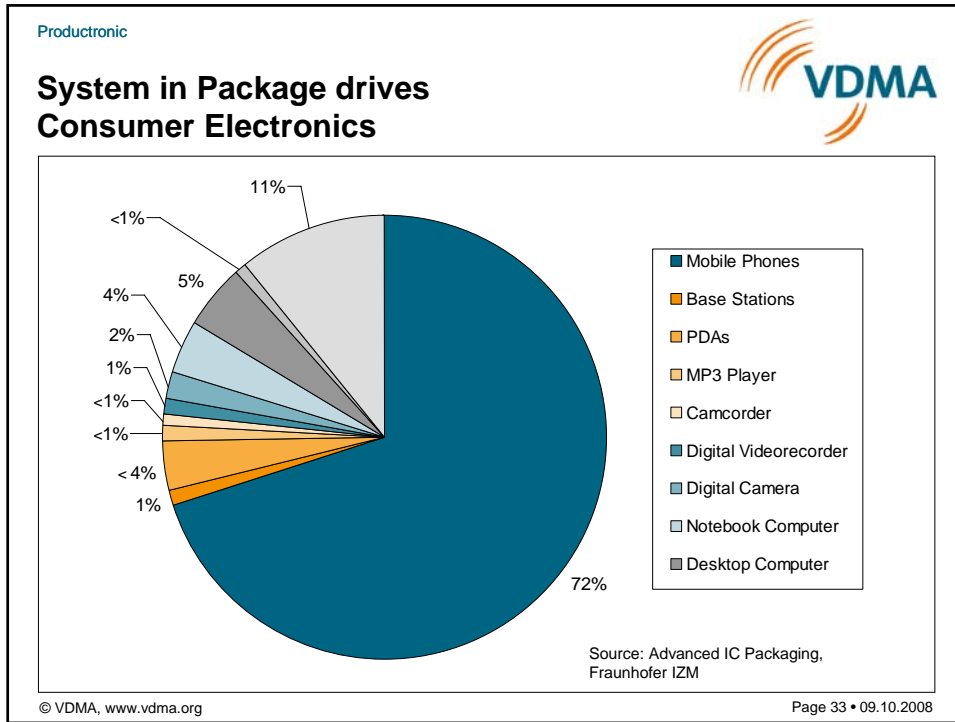


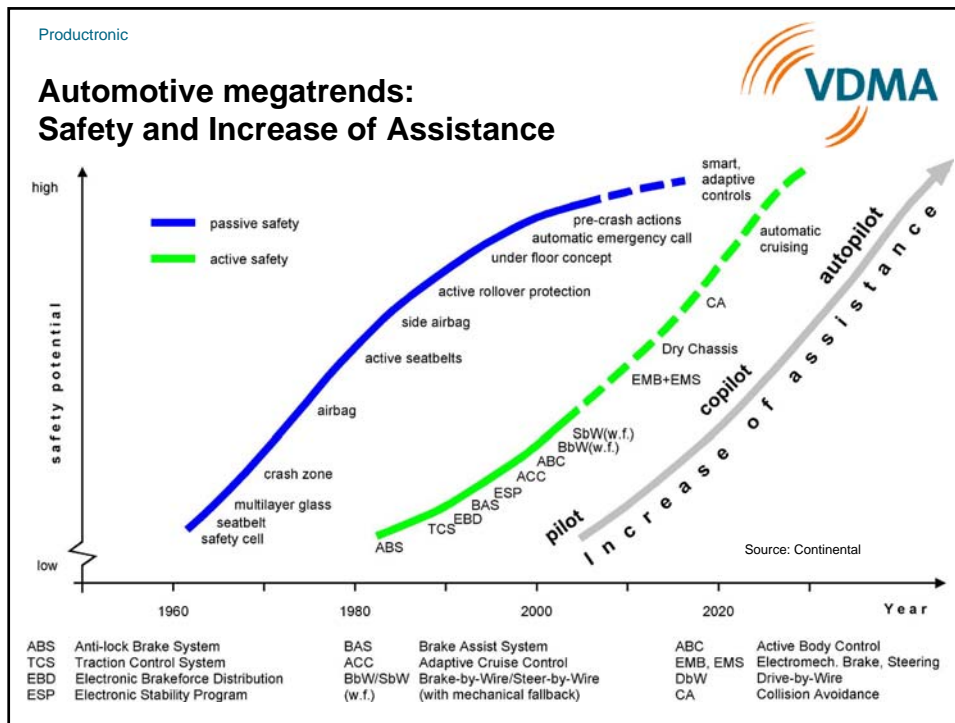
Working Group „Technology Scouting“ Roadmap-Coaching

- Electronics roadmaps interpreted for equipment makers
- Focus on SiP, Hetero integration
- Coached by Fraunhofer IZM  Institut Zuverlässigkeit und Mikrointegration
- Metaplan discussion results, e.g.
 - Wafer-Level-Integration (Thinned Chips, Large-Area-Placement, 3D)
 - Integration into PCB (Chip-in-Polymer, Self-organising production – SOPRO new design needs)
 - Packaging technologies (Organic Electronics, changes in value chain, Tolerance chains for Chip-alignment)
 - Design for Manufacturability / Design for Testability
 - Develop machine interfaces together with customers, standards



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Conclusions

- **Manufacturing equipment Industry is enabler of mass production and pillar of European economy**
- **Equipment makers are SMEs in average**
- **Equipment makers are used to make business outside Europe (Export ratio ~80% in Germany)...**
- **... especially in electronics**
 - Asian manufacturing: Semi: 66%, PCB: 83%, Flat Panel Displays: 98%
- **With overseas operations Equipment makers secure home base (job creation etc!)**

But

- **Equipment makers need R&D support (roadmap coaching, roadmap participation important)**
- **Equipment makers need feedback from manufacturers**
- **Domestic manufacturing creates factor 3-7 jobs in supply chain**
- **If manufacturing moves away, innovation environment (equipment + R&D) moves away, too!**

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